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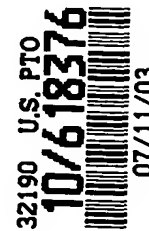
July 11, 2003

Attorney Docket No. MIC-M074

Mail Stop Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Shekar Mallikarjunaswamy
Title: Lateral DMOS Transistor With A Self-Aligned Drain Region



<u>X</u>	Return Receipt Postcard
<u>X</u>	This Transmittal Letter
<u>31</u>	Page(s) Specification including Claims and Abstract
<u>6</u>	Sheet(s) of Drawings (Figs. 1, 2, 3, 4, 5, 6, 7, 8, 9, and 10)
<u>2</u>	Page(s) Declaration For Patent Application and Power of Attorney
<u>1</u>	Page(s) Recordation of Assignment Cover Sheet
<u>1</u>	Page(s) Assignment
<u>1</u>	Page(s) Form PTO-1449
<u>1</u>	Cited Reference

CLAIMS AS FILED

For	Number <u>Filed</u>		Number <u>Extra</u>		Rate		Basic Fee
Total Claims	36	-20 =	16	x	\$18.00 =	\$	750.00
						\$	288.00
Independent Claims	3	-3 =	0	x	\$84.00 =	\$	0.00
<input checked="" type="checkbox"/>	Total fee for filing the patent application in the amount of						\$ 1,038.00
<input checked="" type="checkbox"/>	Fee for Assignment Recordation						\$ 40.00
<input checked="" type="checkbox"/>	Please charge our Deposit Account No. 502226 in the amount of						Total: \$ 1,078.00
<input checked="" type="checkbox"/>	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 502226.						

CORRESPONDENCE ADDRESS

Customer Number



32566

PATENT TRADEMARK OFFICE

EXPRESS MAIL LABEL NO:
EV 325214172 US

Respectfully submitted,

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Reg. No. 42,433